

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HORIGUCHI et al.**

Serial No.: **09/726,386**

Group Art Unit: **2811**

Filed: **December 1, 2000**

Examiner: **Thien F. Tran**

For: **SEMICONDUCTOR MEMORY WITH FLOATING GATE TYPE FET**

RESPONSE TO ELECTION OF SPECIES REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

Date: February 8, 2002

Sir:

This paper is submitted in response to the Official Action dated January 25, 2002.

In the Action, election of a species is required between Embodiment (1) Figs. 1A-1B wherein a second control gate electrode is formed on the dielectric film and electrically connected to the first control gate electrode; Embodiment (2) Figs. 4A-4C wherein the gate insulating film covers upper and side surfaces of the floating gate electrode on the element isolation insulating film; and Embodiment (3) Figs. 6A-6C wherein the sidewall of the floating gate electrode curves so that the floating gate electrode widens as a height thereof from the surface of the semiconductor substrate becomes large.

Applicants hereby elect the subject matter of Embodiment (1), Figs. 1A-1B for prosecution in this application. It is respectfully submitted that claims 1-3 read on the elected species.

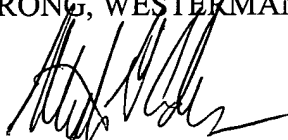
This election is made without traverse, it being understood that the Examiner will consider additional species upon allowance of a generic claim. As such, it is believed that all claims can be examined without undue burden.

In the event that this paper is not timely filed, applicants hereby petition for an appropriate extension of time. The fee for any such extension may be charged to our Deposit Account No. 01-2340.

In the event any additional fees are required in connection with this response, please charge our Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN, , LLP

A handwritten signature in black ink, appearing to read 'Stephen G. Adrian', is written over the printed name.

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